Glenforest Secondary School

Department of Technology and the Arts

Digital Logic Gates Question Sheet

Answer the following questions on the worksheet.

Part I			
Fill in the blanks			
I. In Boolean Logic the numbers I	and (o repre	esent
and			
2. When there are two inputs to a	logic	gate, t	he
truth table will have lines.			
3. The output of the AND gate is I		n	
of the inputs are true.			
4. The output of the OR gate is Iw	hen		of
the inputs are true.			
5. The output of the NOT gate is I	whe	n the i	nput
is			•
6. The output of the NAND gate is of the inputs are	0 w 	hen	
7. The output of the NOR gate is I	whe	n	
of the inputs are			
8. "One or the other but not both'	des	cribes	the
operation of the gate	.		
9. The truth table below correspor		the	
gate.			
	Α	В	X
	0	0	I
	0	ı	0
	ı	0	0
		I	0
10. The truth table below corresponding.	onds	to the	
	Α	В	X
	0	0	0
	0	I	0
	I	0	0
	_ l_	I	LI
II. The truth table below corresponding.	onds	to the	
	Α	В	Χ
	0	0	0
	0		

12. The truth table below	correspond	s 1	to the	
gate.				
	A	\	В	X
	()	0	0
)	ı	Т
	I		0	I
			ı	Ι
Answer the following question 13. Explain the difference OR gate and the Exclusion	between the	е	Inclus	
14. Which gates have a tru	ıth table wit	:h	three	

 zeros and o	one numbe	er I in the	e output (x

15. Which gates have a truth table with three ones

and one zero in the output column?

16. Which gates have a truth table with two zeros and two ones in the output column?

On the next page complete the chart by filling in the symbol, and truth table for each of the eight logic gates.